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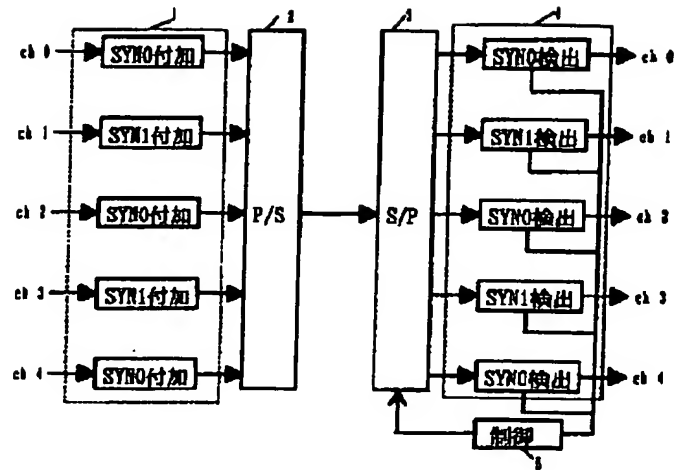
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TITLE : FRAME SYNCHRONIZATION SYSTEM



ABSTRACT : **PURPOSE:** To provide a frame synchronization system by which circuit scale is reduced, a limit for the number of channels to be multiplexed is relaxed, further, power consumption is reduced, a device is miniaturized and costs are decreased.

CONSTITUTION: At a synchronizing pattern adding part 1, two kinds of synchronizing patterns are allocated so as to provide no cyclicity and in the state of normally providing the frame synchronization of serial/parallel conversion, the synchronizing pattern to appear at each channel on the reception side gets equal with the synchronizing pattern added on the transmission side. When the frame synchronization is not provided, however, since the exchange of channels (cyclicity) occurs, the pattern to appear is circulated but since there is no cyclicity in the allocation of two kinds of synchronizing patterns, the synchronizing pattern is detected at all the channels only when the frame synchronization is normally provided. By shifting a frame phase until the synchronizing pattern is detected at all the channels, the frame phase of serial / parallel conversion can be established.

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